

Electronic Acknowledgement Receipt

EFS ID:	5298668
Application Number:	10755042
International Application Number:	
Confirmation Number:	8665
Title of Invention:	Integrated chip package structure using silicon substrate and method of manufacturing the same
First Named Inventor/Applicant Name:	Mou-Shiung Lin
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Filer:	Winston Hsu
Filer Authorized By:	
Attorney Docket Number:	MEGP0004USA1
Receipt Date:	08-MAY-2009
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Application Type:	Utility under 35 USC 111(a)

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed (5B/08)	618746.PDF	738682 <small>5462b65c112d6c127eab04657816c99db52d94</small>	no	4

Warnings:

Information:

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2	Foreign Reference	TW_531854.pdf	1757971 a2637c2d863d87f953d4fc159d5d8ec575280c	no	32
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Information:

3	NPL Documents	NPL1.PDF	530067 b84eeccf3755f82b2d4f2c118d86a1907729d72c	no	3
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4	NPL Documents	NPL2.PDF	1433670 a1399a08f8c3e1e2f6533a6b7f8cc777a406b3c	no	7
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5	NPL Documents	NPL3.pdf	725052 67e4d8579ec8714363b3ba9d318cd89fc7a36c	no	4
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